## LINEAR SYSTEMS

## Linear Integrated Systems

## 2N4351

| FEATURES |  |
| :--- | :---: |
| DIRECT REPLACEMENT FOR INTERSIL 2N4351 |  |
| HIGH DRAIN CURRENT | $\mathrm{I}_{\mathrm{D}}=100 \mathrm{~mA}$ |
| HIGH GAIN | $\mathrm{g}_{\mathrm{fs}}=1000 \mu \mathrm{~S}$ |
| ABSOLUTE MAXIMUM RATINGS ${ }^{1}$ |  |
| @ $25^{\circ} \mathrm{C}$ (unless otherwise stated) |  |
| Maximum Temperatures |  |
| Storage Temperature |  |
| Operating Junction Temperature | -65 to $+200^{\circ} \mathrm{C}$ |
| Maximum Power Dissipation | -55 to $+150^{\circ} \mathrm{C}$ |
| Continuous Power Dissipation |  |
| Maximum Current | 375 mW |
| Drain to Source |  |
| Maximum Voltages | 100 mA |
| Drain to Body |  |
| Drain to Source | 25 V |
| Peak Gate to Source ${ }^{2}$ | 25 V |
|  |  |

TO-72 BOTTOM VIEW


* Body tied to Case.

ELECTRICAL CHARACTERISTICS @ $25^{\circ} \mathrm{C}$ (unless otherwise stated) ( $\mathrm{V}_{\mathrm{SB}}=0 \mathrm{~V}$ unless otherwise stated)

| SYMBOL | CHARACTERISTIC | MIN | TYP | MAX | UNITS | CONDITIONS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| BV ${ }_{\text {DSS }}$ | Drain to Source Breakdown Voltage | 25 |  |  | V | $\mathrm{I}_{\mathrm{D}}=10 \mu \mathrm{~A}, \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V}$ |
| $V_{\text {DS(on) }}$ | Drain to Source "On" Voltage |  |  | 1 |  | $\mathrm{I}_{\mathrm{D}}=2 \mathrm{~mA}, \mathrm{~V}_{\mathrm{GS}}=10 \mathrm{~V}$ |
| $V_{G S(t h)}$ | Gate to Source Threshold Voltage | 1 |  | 5 |  | $V_{D S}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=10 \mu \mathrm{~A}$ |
| Igss | Gate Leakage Current |  |  | 10 | pA | $\mathrm{V}_{G S}= \pm 30 \mathrm{~V}, \mathrm{~V}_{\mathrm{DS}}=0 \mathrm{~V}$ |
| Idss | Drain Leakage Current "Off" |  |  | 10 | nA | $V_{D S}=10 \mathrm{~V}, \mathrm{~V}_{G S}=0 \mathrm{~V}$ |
| $\mathrm{l}_{\mathrm{D} \text { (on) }}$ | Drain Current "On" | 3 |  |  | mA | $V_{G S}=10 \mathrm{~V}, \mathrm{~V}_{\text {DS }}=10 \mathrm{~V}$ |
| $\mathrm{gfs}^{\text {f }}$ | Forward Transconductance | 1000 |  |  | $\mu \mathrm{S}$ | $\mathrm{V}_{\mathrm{DS}}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=2 \mathrm{~mA}, f=1 \mathrm{MHz}$ |
| rDS(on) | Drain to Source "On" Resistance |  |  | 300 | $\Omega$ | $\mathrm{V}_{\mathrm{GS}}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=0 \mathrm{~A}, f=1 \mathrm{kHz}$ |
| $\mathrm{C}_{\text {rss }}$ | Reverse Transfer Capacitance |  |  | 1.3 | pF | $V_{\text {DS }}=0 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V}, f=140 \mathrm{kHz}$ |
| $\mathrm{C}_{\text {iss }}$ | Input Capacitance |  |  | 5.0 |  | $\mathrm{V}_{\mathrm{DS}}=10 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V}, f=140 \mathrm{kHz}$ |
| $\mathrm{C}_{\mathrm{db}}$ | Drain to Body Capacitance |  |  | 5.0 |  | $V_{\text {DB }}=10 \mathrm{~V}, f=140 \mathrm{kHz}$ |

## SWITCHING CHARACTERISTICS

| SYMBOL | CHARACTERISTIC | MAX | UNITS |
| :---: | :--- | :---: | :---: |
| $\mathrm{t}_{\mathrm{d}(\mathrm{on})}$ | Turn On Delay Time | 45 |  |
| $\mathrm{t}_{\mathrm{r}}$ | Turn On Rise Time | 65 | ns |
| $\mathrm{t}_{\mathrm{d}(\mathrm{fff})}$ | Turn Off Delay Time | 60 |  |
| $\mathrm{t}_{\mathrm{f}}$ | Turn Off Fall Time | 100 |  |

## SWITCHING TEST CIRCUIT



TIMING WAVEFORMS


## TO-72

Four Lead


[^0]This datasheet has been download from:
www.datasheetcatalog.com
Datasheets for electronics components.


[^0]:    1. Absolute maximum ratings are limiting values above which serviceability may be impaired.
    2. Device must not be tested at $\pm 125 \mathrm{~V}$ more than once or longer than 300 ms .

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