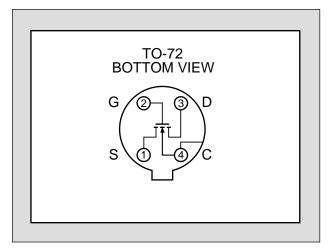


# Linear Integrated Systems

### **FEATURES DIRECT REPLACEMENT FOR INTERSIL 2N4351** HIGH DRAIN CURRENT $I_D = 100 \text{mA}$ HIGH GAIN $g_{fs} = 1000 \mu S$ ABSOLUTE MAXIMUM RATINGS<sup>1</sup> @ 25 °C (unless otherwise stated) **Maximum Temperatures** Storage Temperature -65 to +200 °C **Operating Junction Temperature** -55 to +150 °C **Maximum Power Dissipation** Continuous Power Dissipation 375mW **Maximum Current** Drain to Source 100mA **Maximum Voltages** 25V Drain to Body Drain to Source 25V Peak Gate to Source<sup>2</sup> ±125V

# **2N4351**

# **N-CHANNEL MOSFET ENHANCEMENT MODE**



<sup>\*</sup> Body tied to Case.

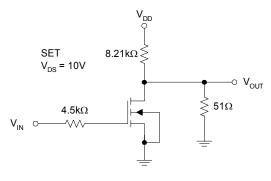
# ELECTRICAL CHARACTERISTICS @ 25 °C (unless otherwise stated) (V<sub>SB</sub> = 0V unless otherwise stated)

SYMBOL	CHARACTERISTIC	MIN	TYP	MAX	UNITS	CONDITIONS
BV <sub>DSS</sub>	Drain to Source Breakdown Voltage	25				$I_D = 10 \mu A, V_{GS} = 0 V$
$V_{DS(on)}$	Drain to Source "On" Voltage			1	V	$I_D = 2mA$ , $V_{GS} = 10V$
$V_{GS(th)}$	Gate to Source Threshold Voltage	1		5		$V_{DS} = 10V, I_{D} = 10\mu A$
I <sub>GSS</sub>	Gate Leakage Current			10	pА	$V_{GS} = \pm 30V, V_{DS} = 0V$
I <sub>DSS</sub>	Drain Leakage Current "Off"			10	nA	$V_{DS} = 10V, V_{GS} = 0V$
$I_{D(on)}$	Drain Current "On"	3			mA	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 10V
<b>9</b> fs	Forward Transconductance	1000			μS	$V_{DS} = 10V, I_{D} = 2mA, f = 1MHz$
r <sub>DS(on)</sub>	Drain to Source "On" Resistance			300	Ω	$V_{GS} = 10V, I_D = 0A, f = 1kHz$
C <sub>rss</sub>	Reverse Transfer Capacitance			1.3		$V_{DS} = 0V, V_{GS} = 0V, f = 140kHz$
$C_{iss}$	Input Capacitance			5.0	pF	$V_{DS} = 10V, V_{GS} = 0V, f = 140kHz$
$C_{\sf db}$	Drain to Body Capacitance		·	5.0		V <sub>DB</sub> = 10V, <i>f</i> = 140kHz

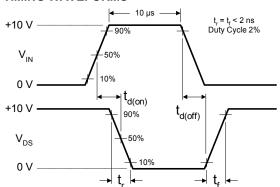
#### **SWITCHING CHARACTERISTICS**

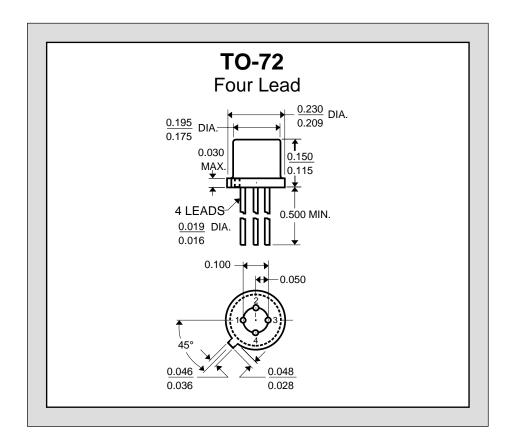
SYMBOL	CHARACTERISTIC	MAX	UNITS
t <sub>d(on)</sub>	Turn On Delay Time	45	
t <sub>r</sub>	Turn On Rise Time	65	ns
$t_{d(off)}$	Turn Off Delay Time	60	
t <sub>f</sub>	Turn Off Fall Time	100	

## **SWITCHING TEST CIRCUIT**



## **TIMING WAVEFORMS**





- Absolute maximum ratings are limiting values above which serviceability may be impaired.
- Device must not be tested at ±125V more than once or longer than 300ms.

Information furnished by Linear Integrated Systems is believed to be accurate and reliable. However, no responsibility is assumed for its use; nor for any infringement of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Linear Integrated Systems.

This datasheet has been download from:

www.datasheetcatalog.com

Datasheets for electronics components.